



GR 98 P 8041

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CERTIFICATION OF MAILING

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By: [Signature]

Date: Oct 15, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 09/313,424 Confirmation No. 3890
Applicant : Thomas Huttner et al.
Filed : May 17, 1999
Title : SOI Semiconductor Configuration And Method Of
Fabricating The Same
Examiner : Brook Kebede
Docket No. : GR 98 P 8041
Group Art Unit : 2823
Customer No. : 24131

A M E N D M E N T under 37 C.F.R. § 1.116

Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S i r :

Responsive to the final Office action dated July 15, 2003,
kindly amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of
claims that begins on page 2 of this paper.

Remarks/Arguments begin on page 7 of this paper.

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